



Form PTO-1449

U.S. DEPARTMENT OF COMMERCE  
PATENT AND TRADEMARK OFFICEATTY. DOCKET NO.  
39439SERIAL NO.  
10/566,456INFORMATION DISCLOSURE CITATION  
BY APPLICANT  
(USE SEVERAL SHEETS IF NECESSARY)APPLICANT:  
Roberto CavazzoniFILING DATE:  
January 30, 2006GROUP ART UNIT:  
2816

## U.S. PATENT DOCUMENTS

Examiner Initial		Document No.	Date	Name	Class	Subclass	Filing Date if Appropriate
<i>DE</i>	A	3,999,137	12/1976	Fucito			
<i>DE</i>	B	4,453,132	5/1984	Stamler			
	C		6/1984				
	D						
	E						
	F						
	G						
	H						
	I						
	J						
	K						

## FOREIGN PATENT DOCUMENTS

		Document No.	Date	Country	Class	Subclass	Translation
<i>DE</i>	L	RE 98U 000057	12/1998	Italy			English Abstract
	M						
	N						
	P						
	Q						

## OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)

<i>DE</i>	R	Soliman, Ahmed M. and Ismail, Mohammed, "A UNIVERSAL VARIABLE PHASE 3-PORT VCVS AND ITS APPLICATION IN TWO-INTEGRATOR LOOP FILTERS"; XP-002240261; IEEE, C+1564-4/80/0000-0083500.75, 1980; P.D. 00-00-1920, pp. 83-86
	S	
	T	

Examiner:

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Initial if reference considered, regardless of whether citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.